

Notic of Allowability	Application No.	Applicant(s)	
	10/616,426	616,426 ABE ET AL.	
	Examiner	Art Unit	
	My-Trang N. Ton	2816	
The MAILING DATE of this communication at All claims being allowable, PROSECUTION ON THE MERITS herewith (or previously mailed), a Notice of Allowance (PTOLNOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT of the Office or upon petition by the applicant. See 37 CFR 1.	is (OR REMAINS) CLOSED in -85) or other appropriate commu T RIGHTS This application is s	this application. If not inc	luded
1. Mail This communication is responsive to Tel. Int. 05/14/04.			•
2. The allowed claim(s) is/are 1-18.		•	
3. The drawings filed on 09 July 2003 are accepted by the	e Examiner.	•	
4. ☑ Acknowledgment is made of a claim for foreign priority a) ☑ All b) ☐ Some* c) ☐ None of the: 1. ☑ Certified copies of the priority documents h	ave been received.		
2. Certified copies of the priority documents h			
3. Copies of the certified copies of the priority	documents have been received	in this national stage appl	ication from the
International Bureau (PCT Rule 17.2(a)).			
* Certified copies not received:			• *
Applicant has THREE MONTHS FROM THE "MAILING DAT noted below. Failure to timely comply will result in ABANDO THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	E" of this communication to file NMENT of this application.	a reply complying with the	requirements
5. A SUBSTITUTE OATH OR DECLARATION must be su INFORMAL PATENT APPLICATION (PTO-152) which (bmitted. Note the attached EXAl gives reason(s) why the oath or	MINER'S AMENDMENT or declaration is deficient.	NOTICE OF
6. CORRECTED DRAWINGS (as "replacement sheets") n	nust be submitted	•	
(a) ☐ including changes required by the Notice of Draftsp	erson's Patent Drawing Review	(PTO-948) attached	
1) ☐ hereto or 2) ☐ to Paper No./Mail Date	·	• •	*, *
(b) ☐ including changes required by the attached Examin Paper No./Mail Date	er's Amendment / Comment or i	n the Office action of	
Identifying indicia such as the application number (see 37 CFF each sheet. Replacement sheet(s) should be labeled as such i	R 1.84(c)) should be written on the	drawings in the front (not t	he back) of
7. DEPOSIT OF and/or INFORMATION about the department attached Examiner's comment regarding REQUIREMENT	posit of BIOLOGICAL MATER	PIAL must be submitted	. Note the
			•.
Attachment(s)			· .
1. Notice of References Cited (PTO-892)	5. Notice of Info	rmal Patent Application (P	TO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948		nmary (PTO-413),	
3. Information Disclosure Statements (PTO-1449 or PTO/SE Paper No./Mail Date 05/17/04	3/08), 7. ⊠ Examiner's A	ail Date <u>05/17/04</u> . mendment/Comment	·
4. Examiner's Comment Regarding Requirement for Deposit	t 8. 🛭 Examiner's S	atement of Reasons for Al	lowance
of Biological Material	9. 🔲 Other		
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EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Att. Schmidt on 05/14/04.

The application has been amended as follows:

In the claims

In claim 1,

line 12, after "shifted", insert -- target clamp --;

line 18, after "terminal", delete - of the first --;

line 19, delete "transistor".

In claim 10,

line 12, after "shifted", insert -- target clamp --;

last line, after "terminal", delete - of the first transistor --

Reasons for allowance

The following is an examiner's statement of reasons for allowance:

The present invention is drawn to a clamp circuit utilizing a first transistor, a buffer circuit, a second transistor and a constant current circuit recited in claims 1-18.

None of the prior art disclosed or suggested to show the particular structure and/or the

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particular operation recited in these claims namely: the combination structure of "the first transistor", "the buffer circuit" and "the second transistor" as recited in claims 1 and 10.

The prior art references submitted by the applicant on IDS form received on 07/09/03 was reviewed and considered. These references appear to be the best references with respect to the claimed invention and closely matched most of the references found during the examiner's searches. However, as noted above, the combination structure of "the first transistor", "the buffer circuit" and "the second transistor" is not disclosed. Therefore, the claims are patentably distinct over all these prior art references of record.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to My-Trang N. Ton whose telephone number is 571-272-1754. The examiner can normally be reached on 7:00 a.m - 5:30 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

MY-TRANGINUTON

PRIMARY EXAMINER

May 17, 2004